



**Product Information** 

**DATE: 05. Apr. 2012** 

SAMSUNG TFT-LCD

MODEL: LTA460HN05-W

The Information Described in this Specification is Preliminary and can be changed without prior notice

Samsung Display Co., LTD.

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Doc. No

LTA460HN05

MODEL



# \* Revision History

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Date	Rev. No	Page	Summary
5, Apr. 2012	000	All	First Issued.

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## **General Description**

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#### Description

LTA460HN05 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 46.0" is 1,920 x 1,080 and this model can display up to 1.07 Billion colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV.

#### **Features**

- RoHS compliance (Pb-free)
- High contrast ratio & aperture ratio with wide color gamut
- SPVA(Super Patterned Vertical Align) mode
- Wide viewing angle (±178°)
- High speed response
- FHD resolution (16:9)
- Low Power consumption
- Edge Type LED (Light Emitting Diode) BLU
- DE (Data Enable) mode
- 2ch LVDS (Low Voltage Differential Signaling) interface (2pixel/clock)

#### **General Information**

Items	Specification	Unit	Note
Madula Ciza	1045.9(W) x 602.1(V)		±1.0mm
Module Size	34.2 (Max)	. mm	T/C~Converter
Weight	12,500	g	Max
Pixel Pitch	0.53(H) x 0.53(V)	mm	
Active Display Area	1018.08(H) x 572.67(V)	mm	
Surface Treatment	Haze 2 %	-	Anti Glare
Display Colors	1.07 Billion	colors	8bit FRC
Number of Pixels	1,920 x 1,080	pixel	
Pixel Arrangement	RGB vertical stripe	-	
Display Mode	Normally Black	-	
Luminance of White	350 (Typ.)	cd/m <sup>2</sup>	

# 1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

 $(V_{DD}=12V)$ 

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	$V_{DD}$	GND-0.3	16	V	(1)
Storage temperature	T <sub>STG</sub>	-20	60	C	(2)
Operation temperature	T <sub>OPR</sub>	0	50	C	(2)
Shock ( non - operating )	S <sub>nop</sub>	-	30	G	(4)
Vibration ( non - operating )	$V_{nop}$	-	1.5	G	(5)

Note (1) Ta= 25  $\pm$  2 °C

- (2) Temperature and relative humidity range are shown in the figure below.
  - a. 90 % RH Max. (Ta ≤ 39 °C)
  - b. Relative Humidity is 90% or less. (Ta > 39 °C)
  - c. No condensation
- (3) Although abnormal visual problems can be occurred in Tsur range, the polarizer is not damaged in this range.
- (4) 11ms, sine wave, one time for  $\pm X$ ,  $\pm Y$ ,  $\pm Z$  axis
- (5) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

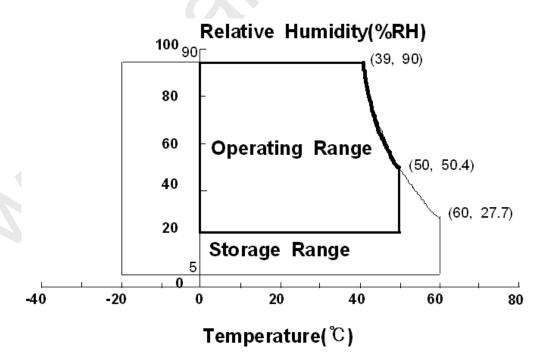


Fig. Temperature and Relative humidity range

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## 2. Optical Characteristics

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The optical characteristics should be measured in a dark room or equivalent. Measuring equipment: TOPCON RD-80S, TOPCON SR-3, ELDIM EZ-Contrast

(Ta =  $25 \pm 2$ °C, VDD=12V, fv= 60Hz, fDCLK = 148.5 MHz, LED Current = 165 mA)

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R		-	5000	1		(1) SR-3
Response Time	G-to-G	Tg		-	10	16	msec	(3) RD-80S
Luminance of (Center of so		Y <sub>L</sub>		-	350	-	cd/m <sup>2</sup>	(4) SR-3
	Red	Rx	Normal		0.647			
	Neu	Ry	q <b>L,R</b> =0 q <b>U,D</b> =0		0.334		)	
	Green	Gx	Viewing		0.316	<b>\limits</b>		
Color	Green	Gy		TYP.	0.595	TYP.		(5),(6)
Chromaticity (CIE 1931)	Blue	Вх	Angle	-0.03	0.148	+0.03		SR-3
, ,	Diue	Ву			0.059			
	White	Wx			0.280			
	vvriite	Wy			0.290			
Color Gar	nut	-		-	70	1	%	(5)
Color Tempe	rature	-		-	10,000	-	K	SR-3
	l lan	$q_L$		75	89	-		
Viewing	Hor.	$q_R$	C/D>10	75	89	-	Doggoo	(6)
Angle	\/or	q <sub>U</sub>	C/R≥10	75	89	-	Degree	EZ-Contrast
	Ver.	$q_D$		75	89	-		
White Brigh Uniformi (9 Points	ty	B <sub>uni</sub>		-	-	30	%	(2) SR-3

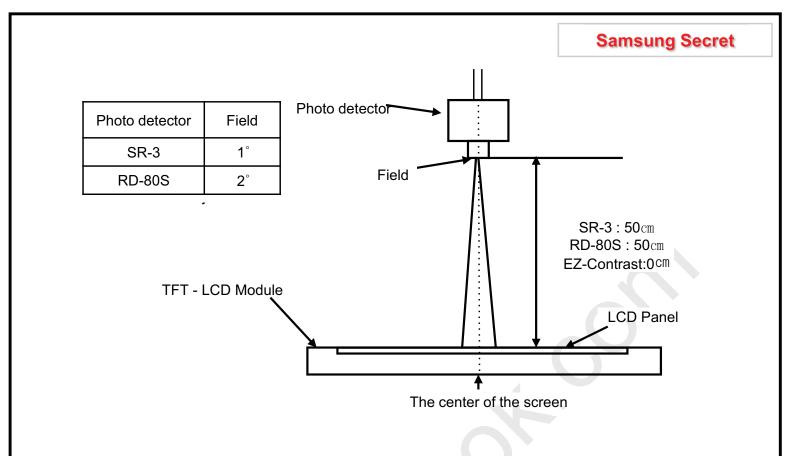
# - Test Equipment Setup

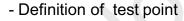
The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

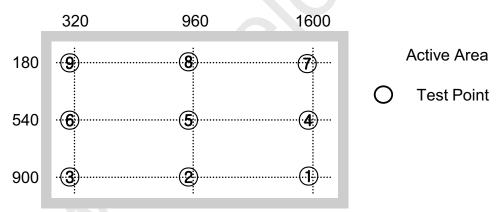
Environment condition : Ta =  $25 \pm 2$  °C

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Note (1) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G \max}{G \min}$$

Gmax : Luminance with all pixels white Gmin : Luminance with all pixels black

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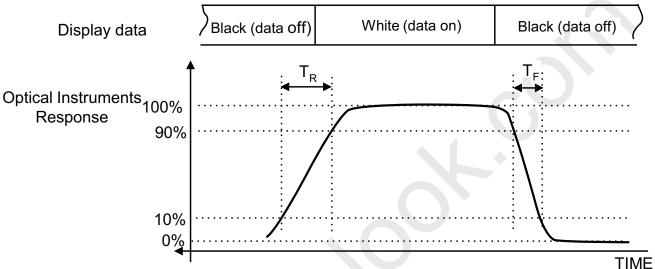
Note (2) Definition of 9 points brightness uniformity (Test pattern: Full White)

$$Buni = 100* \frac{(B \max - B \min)}{B \max}$$

Global LCD Panel Exchange Center

Bmax: Maximum brightness Bmin: Minimum brightness

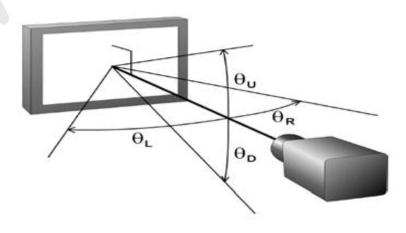
Note (3) Definition of Response time: Sum of Tr, Tf



Note (4) Definition of Luminance of White: Luminance of white at center point (5)

Note (5) Definition of Color Chromaticity (CIE 1931) Color coordinate of Red, Green, Blue & White at center point 5

Note (6) Definition of Viewing Angle : Viewing angle range (C/R ≥10)



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## 3. Electrical Characteristics

#### 3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

Ta =  $25^{\circ}$ C  $\pm$  2  $^{\circ}$ C

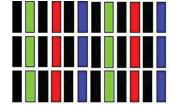
Item		Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of P	ower Supply	V <sub>DD</sub>	10.8	12.0	13.2	V	(1)
Current of	(a) Black		-	279	-	mA	
Power	(b) White	I <sub>DD</sub>	-	251	-	mA	(2),(3)
Supply	(c) Sub V-Stripe		-	532	780	mA	
Vsync Frequ	iency	f <sub>V</sub>	47	60	63	Hz	
Hsync Frequency		f <sub>H</sub>	50	67.5	75	kHz	
Main Frequency		f <sub>DCLK</sub>	130	148.5	160	MHz	
Rush Current		I <sub>RUSH</sub>	-	-	5	Α	(4)

Note (1) The ripple voltage should be controlled under 10% of  $V_{DD}$ .

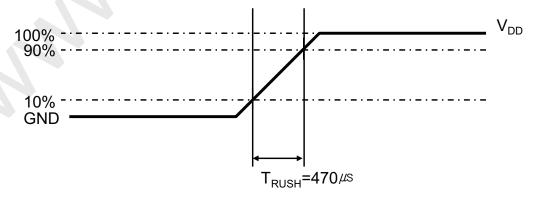
- (2) fv= 60 Hz, fDCLK = 148.5 MHz,  $V_{DD}$  = 12.0V, DC Current.
- (3) Power dissipation check pattern (LCD Module only)
- a) Black Pattern
- b) White Pattern
- c) Sub V-Stripe







(4) Measurement Conditions



Rush Current I<sub>RUSH</sub> can be measured when  $T_{RUSH}$  is 470  $\mu$ s.

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# 3.2 Back Light Unit

The back light unit contains Edge type White LEDs (Light Emitting Diode)

Ta=25  $\pm$  2°C



# **LCD Module**



Item	Symbol	Min.	Тур.	Max.	Unit	Note
Operating Life Time	Hr	-	30,000	1	Hour	(1),(2)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value. [Operating condition : Ta =  $25\pm2^{\circ}$ C, For LED Package only. ]

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# 3.3 Converter Input Condition & Specification

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Items	Symbol	Conditions	Min.	Тур.	Max.	Unit	Note	
Input Voltage	Vin	-	22	24	26	V	Ta=25±2 °C	
Input Current	I <sub>RUSH (2D)</sub>	Vin=24.0V Dim = MAX	-	-	4.8	A	Note (1)	
Output Current	I <sub>O (2D)</sub>	Vin=24.0V Dim = MAX	157	165	173	mA	Note (2)	
Backlight	ON	Vin=24.0 V	2.4		5.5	V		
On/Off	OFF	VIII-24.0 V	-0.3	1	0.8	V		
External Dimming	D <sub>EXT_DIM</sub>	Vin=24.0V	1	-	100	%		
	F <sub>EXT_DIM</sub>	9	95	-	200	HZ Note(3)	Note (4)	
External Dimming		High (ON)	2.4	-	5.5	V		
Signal Level	V <sub>EXT_DIM</sub>	Low (Off)	-0.3	-	0.8	V		

Note) Power Consumption is measured when 350 [cd/m ] of luminance which is the typical luminance.

(1) All data is measured after 120min warm-up.

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# 4. Input Terminal Pin Assignment

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# 4.1. Input Signal & Power

Connector :FI-RE51S-HF (JAE/UJU)

PIN No.		Description	PIN No.		Description				
1		VDD(12V)	26		RE[0]P				
2		VDD(12V)	27	]	RE[1]N				
3		VDD(12V)	28		RE[1]P				
4		VDD(12V)	29		RE[2]N				
5		VDD(12V)	30	]	RE[2]P				
6	No Co	onnection * Note (1)	31	Even	GND				
7		GND	32	LVDS	RE[CLK]N				
8		GND	33	Signal	RE[CLK]P				
9		GND	34	1	GND				
10		RO[0]N	35	1	RE[3]N				
11		RO[0]P	36		RE[3]P				
12		RO[1]N	37	1	RE[4]N				
13		RO[1]P	38		RE[4]P				
14		RO[2]N	39		GND				
15		RO[2]P	40		No Connection				
16	Odd	GND	41		No Connection				
17	LVDS Signal	RO[CLK]N	42		No Connection				
18		RO[CLK]P	43		No Connection				
19		GND	44		No Connection				
20	]	RO[3]N	45	L\	/DS Option *Note (2)				
21		RO[3]P	46		No Connection				
22		RO[4]N	47		No Connection				
23		RO[4]P	48		No Connection				
24		GND	49		No Connection				
25	Even LVDS	RE[0]N	50		No Connection				
			51		No Connection				

Note)(1) No Connection: This PINS are only used for SAMSUNG internal using.

(2) LVDS OPTION: If this PIN: HIGH (3.3 V)  $\rightarrow$  Normal LVDS format

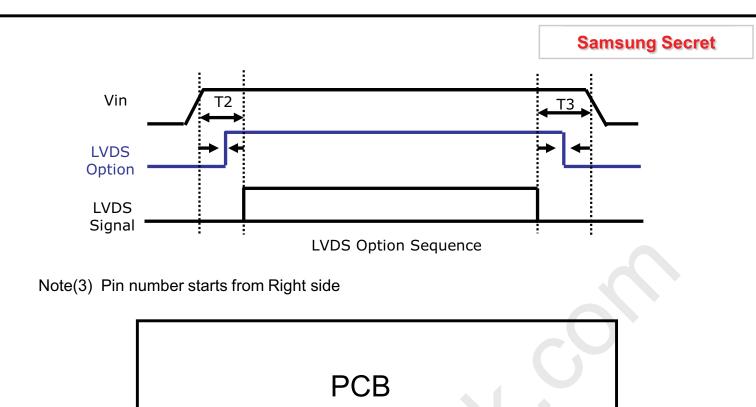
: LOW (GND)  $\rightarrow$  JEIDA LVDS format

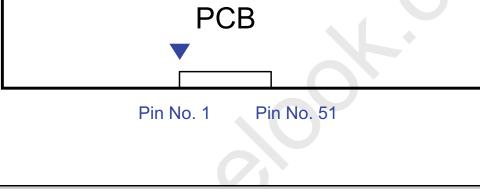
SEQUENCE : On =  $VDD(T1) \ge LVDS$  Option  $\ge Interface$  Signal(T2)

OFF = Interface Signal(T3) ≥ LVDS Option ≥ VDD

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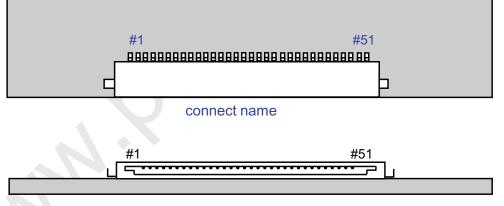


Fig. Connector diagram

- a. All GND pins should be connected together and also be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All NC pins should be separated from other signal or power.

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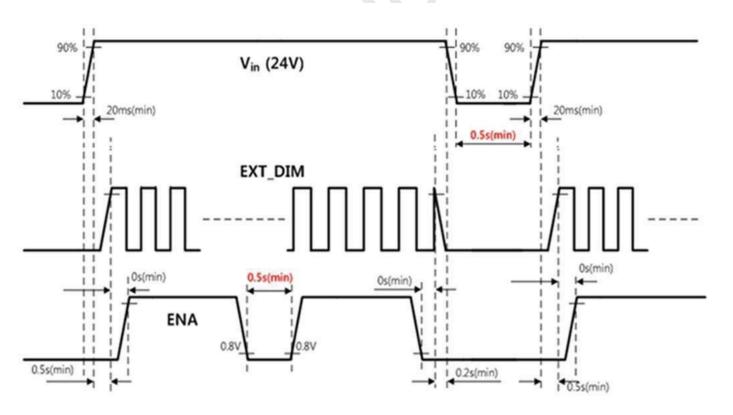
## 4.2. Converter Input Pin Configuration

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Connector: Yeon-ho, 20022WR-14B1

Pin No.	Pin Configuration (FUNCTION)
PIII NO.	Master
1 ~5	24 V
6~10	GND
11	No Connection
12	Backlight On /Off
13	No Connection
14	External PWM

# 4.3. Converter Input Power Sequence



Note) SEQUENCE : ON = Vin(24V) > Dimming Control ≥ Backlight On/Off OFF = Backlight On/Off ≥ Dimming Control > Vin(24V)

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# 4.4 LVDS Interface

- LVDS Receiver : Tcon (merged)

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- Data Format (JEIDA & Normal)

		LVDS p	in		JEIDA -DATA	N	Normal -DATA						
		TxIN/RxO	UT0		R4		R0						
		TxIN/RxO	UT1		R5		R1						
		TxIN/RxO	UT2		R6	R2							
TxOUT/I	RxIN0	TxIN/RxO	UT3		R7	R3							
		TxIN/RxO	UT4		R8		R4						
		TxIN/RxO	UT6		R9		R5						
		TxIN/RxO	UT7		G4		G0						
		TxIN/RxO	UT8		G5		G1						
		TxIN/RxO	UT9		G6		G2						
	TxIN/RxO				G7		G3						
TxOUT/I	RxIN1	TxIN/RxOL	JT13		G8		G4						
	TxIN/RxO				G9	<b>\limits</b>	G5						
	TxIN/RxO				B4		В0						
	TxIN/RxO				B5		B1						
		TxIN/RxOl	JT19		B6		B2						
	TxIN/RxOU TxIN/RxOU				B7		B3						
					B8		B4						
TxOUT/I	RxIN2	TxIN/RxOl	JT22		B9		B5						
		TxIN/RxOL	JT24		HSYNC		HSYNC						
		TxIN/RxOL	JT25		VSYNC		VSYNC						
		TxIN/RxOL	JT26		DEN	DEN							
		TxIN/RxOL	JT27		R2	R6							
		TxIN/RxO	UT5		R3		R7						
		TxIN/RxOL	JT10		G2		G6						
TxOUT/I	RxIN3	TxIN/RxOU	JT11		G3		G7						
		TxIN/RxOU	JT16		B2	B6 B7							
		TxIN/RxOU	JT17		B3								
		TxIN/RxOL	JT23		RESERVED		RESERVE	.D					
		TxIN/RxOL	JT28		R0	R8							
		TxIN/RxOL	JT29		R1		R9						
		TxIN/RxOL	JT30		G0		G8						
TxOUT/I	RxIN4	TxIN/RxOL	JT31		G1		G9						
		TxIN/RxOL	JT32		B0		B8						
		TxIN/RxOL	JT33		B1		B9						
	TxIN/RxOUT34				RESERVED		RESERVE	D					
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# 4.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

	riput Siç															TA S																GRAY
COLOR	DISPLAY (10bit)					RI	ED									GRE	EEN									BL	UE					SCALE
	,	R0	R1	R2	R3	R4	R5	R6	R7	R8	R9	G0	G1	G2	G3	G4	G5	G6	G7	G8	G9	В0	B1	B2	В3	B4	B5	В6	В7	В8	В9	LEVEL
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	ı
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	ı
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-
BASIC	CYAN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
COLOR	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
		1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
	DARK	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
GRAY SCALE	<b>1</b>	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:				:	:	:	:	:	:	:	:	:	:	R3~
OF RED	<b>\</b>	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R1020
	LIGHT	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1021
		0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1022
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1023
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
		0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
	DARK	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
GRAY SCALE	<b>↑</b>		• •	:	• •	:	:				٠.	<u> </u>		• •	• •			:	:	:			:				:	:	:	:	:	G3~
OF GREEN	<b>\</b>	:	:	:	:	:	: ,	:			9.	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G1020
	LIGHT	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1021
		0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1022
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1023
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	В0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	B1
	DARK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	B2
GRAY SCALE	1		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~
OF BLUE	1	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B1020
	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	B1021
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	B1022
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	B1023

Note) Definition of Gray:

Rn: Red Gray, Gn: Green Gray, Bn: Blue Gray (n = Gray level)

Input Signal: 0 = Low level voltage, 1 = High level voltage

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# 5. Interface Timing

# 5.1 Timing Parameters (DE mode)

			_				
SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock		1/T <sub>C</sub>	130	148.5	160	MHz	-
Hsync	Frequency	F <sub>H</sub>	50.0	67.5	75.0	KHz	-
Vsync		F <sub>V</sub>	47.0	60.0	63.0	Hz	-
Vertical	Active Display Period	T <sub>VD</sub>	-	1080	-	Lines	-
Display Term	Vertical Total	T <sub>V</sub>	1092	1125	1380	Lines	-
Horizontal	Active Display Period	T <sub>HD</sub>	- (	1920	<u> -</u>	Clocks	-
Display Term	Horizontal Total	T <sub>H</sub>	2090	2200	2350	clocks	-

Note) This product is DE mode. But the Hsync & Vsync signal must be inputted

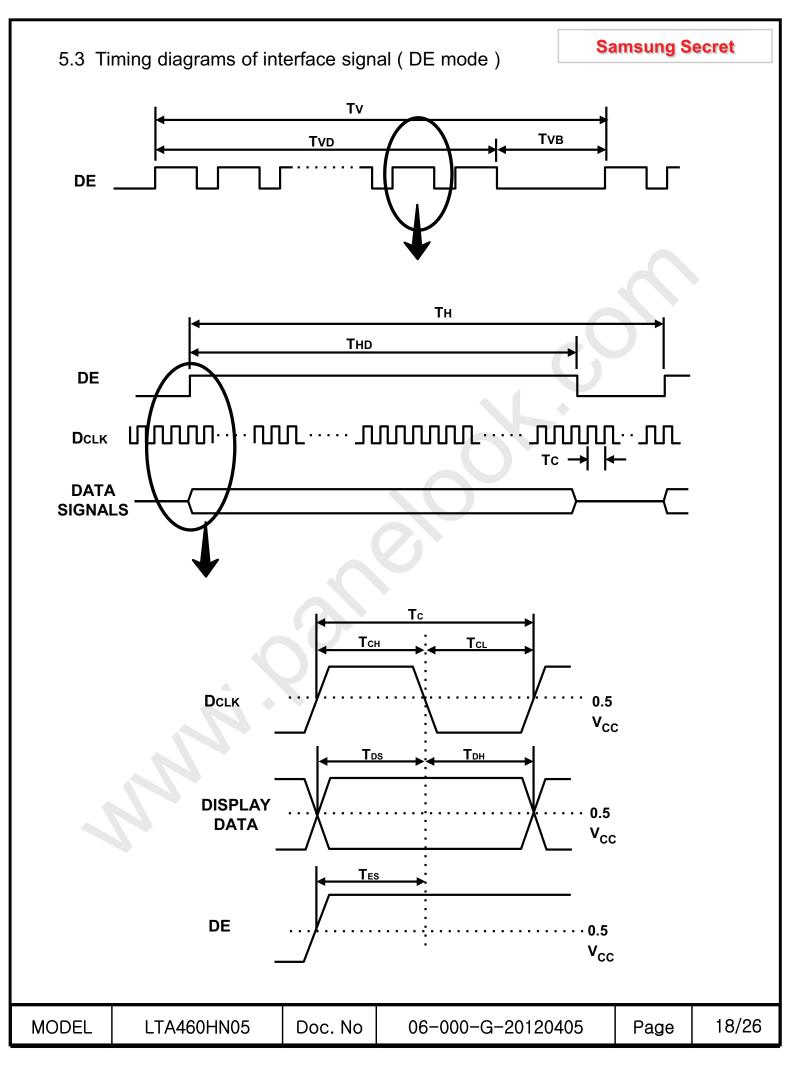
- (1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system
- (2) Internal VDD = 3.3V
- (3) Spread spectrum
  - Modulation rate (max) :  $\pm$  1.5 %
  - Modulation Frequency : 30~ 100KHz

## 5.2 LVDS Input Data Characteristics

ITEM		SYMBOL	Min.	Тур.	Max.	UNIT	NOTE
Input Data Position F <sub>IN</sub> =75MH	E -75MU-	t <sub>RSRM</sub>	ı	1	500	ps	
	F <sub>IN</sub> -7 SIVIEZ	t <sub>RSLM</sub>	-500	1	ı	ps	
Input common	mode voltage	V <sub>CM</sub>	0.55	-	1.8	V	-
Differential Input Voltage		V <sub>ID</sub>	100	-	-	mV	-

Note) When the skew is measured the Spread Spectrum should be 0%

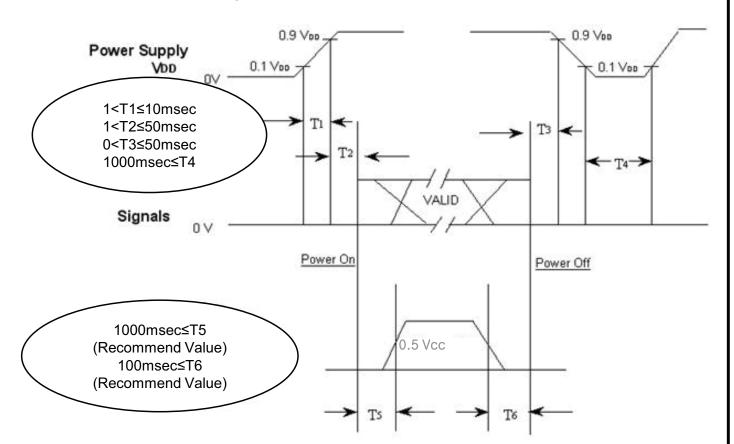
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## 5.4 Power ON/OFF Sequence

### Samsung Secret

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T1: V<sub>DD</sub> rising time from 10% to 90%

T2 : The time from  $V_{DD}$  to valid data at power ON.

T3 : The time from valid data off to  $V_{DD}$  off at power Off.

T4: V<sub>DD</sub> off time for Windows restart

T5: The time from valid data to B/L enable at power ON.

T6: The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of  $V_{DD}$ .
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V<sub>DD</sub> = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.
- In Case T5 is less than 1000msec and T6 is less than 100msec, Garbage Display can be seen. (It is not related to electrical function issue, Just for recommendation to prevent Garbage Display)

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6. Outline Dimension- Front

**Samsung Secret** 

TBD

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6. Outline Dimension-Rear

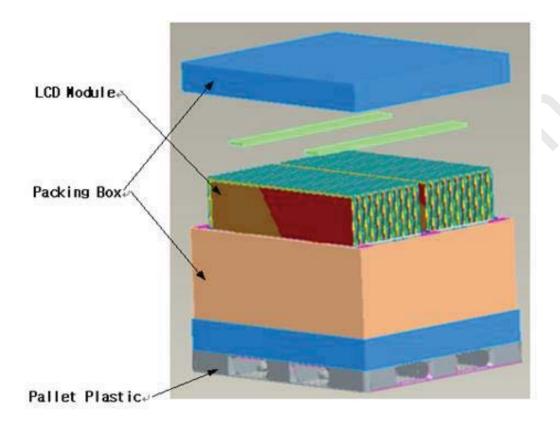
**Samsung Secret** 

TBD

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## 7. PACKING

# 7.1 CARTON (Internal Package)



# 7.2 Packing Specification

Item	Specification	Remark
LCD Packing	24 ea / Box (Packing-Pallet Box)	<ol> <li>1. 11.5 / LCD (24ea)</li> <li>2. 24.1Kg / Packing Box (1ea)</li> <li>&gt;. Packingt Box Material : KLB, AK, CK</li> <li>3. 60g/Slica-Gel (6ea)</li> </ol>
Pallet	1Box / Pallet (PE,W1270,L1150,H125)	1. Pallet weight = 7.8Kg >. Pallet Material : HDPE
Packing Direction	Vertical	
Total Pallet Size	H x V x height	<b>1270</b> mm(H) x <b>1150</b> mm(V) x <b>788</b> mm(height)
Total Pallet Weight 309.34 kg		Pallet (7.8kg) + Module (276kg) + Packing-Box (24.1kg) + silica-gel(1.44Kg)

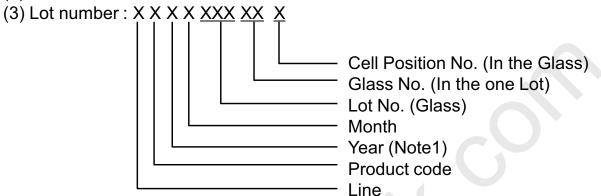
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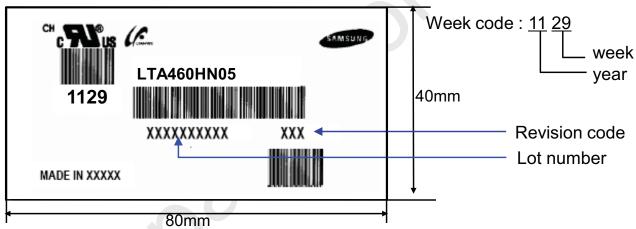
## 8. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

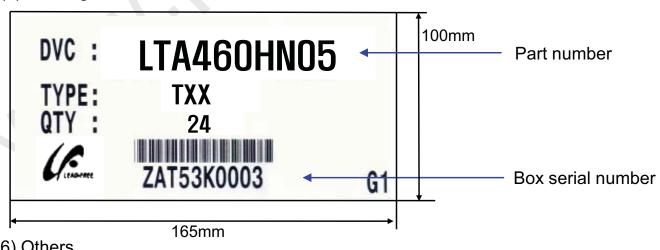
- (1) Part number: LTA460HN05
- (2) Revision: Three letters



## (4) Nameplate Indication



#### (5) Packing box attach



- (6) Others
- 1. After service part

Lamps cannot be replaced because of the narrow bezel structure.



#### 9. General Precautions

#### Samsung Secret

- 9.1 Handling
- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFT back light.
- (d) Note that polarizers are very fragile and could be damage easily.

  Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or semiconductor would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (I) Do not disassemble shield case of inverter & LVDS board
- (m) Do not connect N.C pins. (Samsung internal use only)
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handling a module
- (o) Pins of I/F connector should not be touched directly with bare hands.

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## 9.2 Storage

Item	Unit	Min.	Max.	
Storage Temperature	(℃)	5	40	
Storage Humidity	(%rH)	35	75	
Storage Life	6 Months			
Storage Condition	<ul> <li>The storage room should provide good ventilation and temperature control.</li> <li>Products should not be placed on the floor, but on the Pallet away from a wall.</li> <li>Prevent products from direct sunlight, moisture nor water; Be cautious of a build up of condensation.</li> <li>Avoid other hazardous environment while storing goods.</li> <li>If products delivered or kept in conditions of over the storage period of 3 months, the recommended temperature or humidity range, we recommend you leave them at a temperature of 20 °C and a humidity of 50% for 24 hours.</li> </ul>			

# 9.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(LED) and may require higher startup voltage(Vs).



## 9.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions.

Normal condition is defined as below;

- Temperature :  $20\pm15\,^{\circ}\mathrm{C}$ 

- Humidity :  $55\pm20\%$ 

- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

#### 9.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. ( supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)

Otherwise the Module may be damaged.

- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.

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